

What Is Claimed Is:

1. A set of peripheral chips for realizing hardware functions of a control device, comprising:
 - at least two electronic units that ensure a partitioning so as to provide a basic functionality of the control device, the at least two electronic units including a first electronic unit and a second electronic unit,
 - wherein at least one of:
 - (a) the first electronic unit includes:
 - a voltage supply having different output voltages;
 - at least one transducer supply;
 - a monitoring module;
 - a driver for bi-directional serial interfaces;
 - a CAN driver;
 - a primary relay control;
 - a primary relay output stage;
 - a serial peripheral interface; and
 - a stop/wake-up counter, and
 - (b) the second electronic unit includes:
 - a power output stage;
 - at least one low-level signal output stage; and
 - a voltage monitor.
2. The set of peripheral chips according to claim 1, wherein the at least one transducer supply includes three transducer supplies.
3. The set of peripheral chips according to claim 1, wherein the CAN driver has a wake-up function.
4. The set of peripheral chips according to claim 1, wherein the power output stage is an eighteen-fold power output stage having rated currents between 0.6A and 3A.

5. The set of peripheral chips according to claim 1, wherein the at least one low-level signal output stage includes two low-level signal output stages.
6. The set of peripheral chips according to claim 1, wherein the voltage monitor is a five-volt monitor.
7. The set of peripheral chips according to claim 1, further comprising at least one additional electronic unit that allows a partitioning for providing a functionality beyond the basic functionality of the control device, the at least one additional electronic unit including:
 - an analog/digital converter;
 - at least one low-signal output stage; and
 - a driver for bi-directional serial interfaces.
8. The set of peripheral chips according to claim 7, wherein the analog/digital converter has eight channels.
9. The set of peripheral chips according to claim 7, wherein the at least one low-signal output stage includes two low-signal output stages.
10. The set of peripheral chips according to claim 1, further comprising at least one additional electronic unit that allows a partitioning so as to provide a functionality beyond the basic functionality of the control device, the at least one additional electronic unit including an evaluation circuit for inductive transducers.
11. The set of peripheral chips according to claim 1, wherein at least one of the electronic units includes an application-specific electronic switching circuit.
12. The set of peripheral chips according to claim 1, further comprising at least one of a central processing unit and a microcontroller.
13. The set of peripheral chips according to claim 1, further comprising:

- (1) at least one evaluation device for at least one lambda sensor;
 - (2) at least one additional CAN driver;
 - (3) at least one additional power output stage; and
 - (4) at least one output stage specific to an injection system,
- wherein (1), (2), (3), and (4) are accommodated in separate subassemblies.

14. The set of peripheral chips according to claim 1, wherein the at least one transducer supply includes a first transducer supply providing a first voltage of about 5V, a second transducer supply providing a second voltage of about 3.3V, and a third transducer supply providing a voltage switchable between the first voltage and the second voltage.

15. An engine-control device comprising at least one set of peripheral chips, each of the at least one set of peripheral chips including:
at least two electronic units that ensure a partitioning so as to provide a basic functionality of the control device, the at least two electronic units including a first electronic unit and a second electronic unit,

wherein at least one of:

- (a) the first electronic unit includes:
 - a voltage supply having different output voltages;
 - at least one transducer supply;
 - a monitoring module;
 - a driver for bi-directional serial interfaces;
 - a CAN driver;
 - a primary relay control;
 - a primary relay output stage;
 - a serial peripheral interface; and
 - a stop/wake-up counter, and
- (b) the second electronic unit includes:
 - a power output stage;
 - at least one low-level signal output stage; and

a voltage monitor.

16. The engine-control device according to claim 15, wherein the engine-control device is for a motor vehicle.

17. The engine-control device according to claim 15, wherein the engine-control device is for an engine-management system.

18. The engine-control device according to claim 15, wherein the engine-control device is for a fuel-injection system.